Experiment: 4-bit Adder

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## Overview of the experiment:

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| In two to three paragraphs, summarize  • The purpose of the experiment.  • What you did to perform the experiment.  • Organization of your report and a summary of the data you will be presenting.  The purpose of the experiment was to implement and |

## Approach to the experiment:

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| Describe the approach you have used to complete the assignment. You must draw a schematic/block diagram of the design (hand-drawn/software). |

## Design document and VHDL code if relevant:

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| You should give a brief description of whatever designs you have constructed and a sketch (architecture of main logic) of the code you have written as part of the experiment. |

## RTL View:

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| Attach screen-shot of the RTL view generated by Quartus. |

## DUT Input/Output Format:

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| Mention the format (LSB/MSB of input and output) and few test cases from trace-file. |

## RTL Simulation:

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| Attach the clearly visible screen-shot of RTL simulation waveforms. |

## Gate-level Simulation:

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| Attach the clearly visible screen-shot of Gate-level Simulation. |

## Krypton board\*:

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| Map the logic circuit to the Krypton board and attach the images of the pin assignment and output observed on the board (switches/LEDs). |

## Observations\*:

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| You must summarize your observations, either in words, using figures and/or tables. |

## References:

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| You may include the references if any. |

\* To be submitted after the tutorial on ”Using Krypton.